

ABSTRACT

A chip resistor (A1) includes a resistor element (1) including an electrode forming surface (10b), two electrodes (3) provided at the electrode-forming surface (10b), and an insulating layer (2A) provided at the electrode-forming surface (10b). The electrode-forming surface (10b) includes an inter-electrode region positioned between the two electrodes (3) and covered by the insulating layer (2A). The insulating layer (2A) has a thickness (t2) which is equal or generally equal to the thickness (t1) of the electrodes (3).